IVAN M | SOFTWARE ENGINEER

artisignal.com • artisignal@outlook.com

A software development professional operating in the fields of network technology, device drivers and packet processing applications. With necessary knowledge of computer architecture in place, potential lines of work may include development of host software, improvement of firmware and the use of test automation for quality assurance of hardware and software products.

TECHNOLOGY

Programming Languages: C, Assembly, Bash, P4

Network Programming: <u>DPDK</u>, vSwitch, RDMA

• Heterogeneous Computing: ROCm, CUDA

Operating Systems: GNU/Linux

EXPERIENCE

- **Software Engineer** / <u>Ark Networks</u>, Yerevan (hybrid) / 2022 present
 - quality assurance of the customer's RDMA SmartNIC data path improvements
 - development of a DPDK-based test system for the customer's RDMA SmartNIC
 - support for vSwitch and conntrack offloads in the customer's DPDK driver
 - support for the new NIC family in the customer's DPDK driver
 - quality assurance of the new NIC family test automation
- **Software Engineer** / OKTET Labs, Saint Petersburg (hybrid) / 2015 2022
 - support for performance features in one of DPDK drivers
 - development of the in-house test automation framework
 - development of the automated <u>DPDK PMD test suite</u>

EDUCATION

Saint Petersburg State University / 2010 - 2015 / bachelor's degree

Provided assistance to some of research initiatives in the field of <u>computer simulation</u>. To this end, developed programs in Fortran 90, for input data generation, as well as in Perl/CUDA, to make use of a graphics adaptor for processing of simulation outcomes.

CERTIFICATIONS

IELTS for UKVI General Training / March 2025 / CEFR Level: C1

Listening: 8.5 / Reading: 9.0 / Writing: 7.5 / Speaking: 7.0